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08/839,558	04/14/97	711
SERIAL NUMBER	CLASS	SUBCLASS
08/839,558	711	118
GROUP ART UNIT	EXAMINER	
2752	Class	

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CONTINUING DATA*****
VERIFIED WON
Acc

FOREIGN/PCT APPLICATIONS*****
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TITLE
DUAL CACHE DIRECTORIES ALLOWING SIMULTANEOUS READ OPERATIONS
ASSOCIATIVE-CACHE DIRECTORIES ALLOWING SIMULTANEOUS
READ OPERATION USING TWO BUSES WITH MULTIPLE ADDRESS
TAGS MEMORY BLOCK CONTROL SIGNALS SINGLE CLOCK AND
OPERATION AND ERROR CORRECTION
U.S. DEPT. OF COMM./PAT. & TM - PTO-4361 (Rev. 12-84)

PARTS OF APPLICATION FILED SEPARATELY		CLAIMS ALLOWED	
NOTICE OF ALLOWANCE MAILED		Total Claims	
6-21-97		Print Claim	
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1,220.00	9/15/97	4	3
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JOHN W. CABECA SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2700		072	
PREPARED FOR ISSUE			
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AREA

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PATENT NUMBER

ORIGINAL CLASSIFICATION

CLASS

SUBCLASS

711

118

APPLICATION SERIAL NUMBER

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CROSS REFERENCE(S)

CLASS

SUBCLASS
(ONE SUBCLASS PER BLOCK)

711

124

119

113

APPLICANT'S NAME (PLEASE PRINT)

R. Arimilli, et al.

IF REISSUE, ORIGINAL PATENT NUMBER

INTERNATIONAL CLASSIFICATION

G 06 F

12/00

GROUP
ART UNIT

2752

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